

PN Code Generator Peripheral (N=7)

- Features:
- 3 bit Fibonacci Linear Feedback Shift Register
 - Fixed 3 bit Register Contents for Pseudorandom (PN) Sequence Bank 0
 - Variable 3 bit Register Contents for Pseudorandom (PN) Sequence Bank 1
 - 28 Channel Serial PN Code Output Bank 0
 - 28 Channel Serial PN Code Output Bank 1
 - Global Output Enable for all PN Code Channels
 - Local Output Enable for each PN Code Channel
 - Global Enable for all PN Code Channels
 - Local Enable for each PN Code Channel
 - 6 bit Address and 16 bit Microprocessor Interface
 - 4 bit Control and Status Register
 - 3 bit PN Readback
 - Global Enable
 - Global Output Enable
 - 4 bit Output for FIFO Interface
 - 3 bit PN Sequence Register
 - 1 bit PN Output

