

## Gold Code Generator Peripheral

- Features:
- 5 bit Fibonacci Linear Feedback Shift Registers
  - Fixed 5 bit Register Contents for Pseudorandom (PN) Sequence 0
  - Variable 5 bit Register Contents for Pseudorandom (PN) Sequence 1
  - 16 Channel Serial Gold Code Output
  - Global Output Enable for all Gold Code Channels
  - Local Output Enable for all Gold Code Channels
  - Global Enable for all Gold Code Channels
  - Local Enable for all Gold Code Channels
  - 5 bit Address and 16 bit Microprocessor Interface
    - 4 bit PN Sequence 1 Initial Contents
    - 4 bit Control and Status Register
    - 5 bit PN 0 Readback
    - 5 bit PN 1 Readback
    - Global Enable
    - Global Output Enable
  - 16 bit Output for FIFO Interface
    - 5 bit PN Sequence 0 Registers
    - 5 bit PN Sequence 1 Registers
    - 5 bit Gold Code Registers
    - 1 bit Gold Code Output

